

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of  
Shunpei YAMAZAKI et al.

Serial No. 09/233,145

Filed: January 19, 1999

For: ACTIVE MATRIX DISPLAY  
DEVICE



Art Unit: 2871

Examiner: T. Duong

CERTIFICATE OF MAILING	
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AMENDMENT

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

In response to the Office Action dated July 30, 2002 please amend the above-  
identified application as follows:

IN THE CLAIMS:

Please amend claims 56-65 as follows:

56. (Amended) A display device comprising:

at least one thin film transistor formed over a substrate, said thin film transistor  
having a semiconductor film comprising silicon as an active layer thereof and a gate  
electrode adjacent to said semiconductor film with a gate insulating film interposed  
therebetween;

an insulating film comprising an inorganic material formed over said gate  
electrode;

a first contact hole in said insulating film;

a wiring formed on said insulating film and electrically connected to said  
semiconductor film through said first contact hole formed in said insulating film;

a leveling film comprising an organic resin to provide a leveled upper surface  
over said insulating film;

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